



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **WATANABE, Kenichi**

Group Art Unit: **2815** ✓

Serial No.: **10/050,171**

Examiner: **WARREN, Matthew E.**

Filed: **January 18, 2002**

**P.T.O. Confirmation No.: 7623**

For: **SEMICONDUCTOR DEVICE CAPABLE OF SUPPRESSING CURRENT  
CONCENTRATION IN PAD AND ITS MANUFACTURE METHOD**

**AMENDMENT UNDER 37 CFR §1.111**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

December 15, 2003

Sir:

In response to the Office Action dated **September 16, 2003**, please amend the above-identified application as follows:

RECEIVED  
DEC 17 2003  
TC 2800 MAIL ROOM